

TO SEPARATE, HOLD TOP AND BOTTOM EDGES, SNAP-APART AND DISCARD CARBON

FORM PTO-892 (REV. 2-92)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. 08/327887	GROUP/ART UNIT 1107	ATTACHMENT TO PAPER NUMBER 16
NOTICE OF REFERENCES CITED				APPLICANT(S) Kim et al		

U.S. PATENT DOCUMENTS							
*	DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE	
A	4804560	2/89	Shioya et al	427	125		
B	4956313	9/90	Cote et al	437	195		
C	5063175	11/91	Broadbent	437	195	12/88	
D	5100817	3/92	Catbaun et al	437	245	7/91	
E							
F							
G							
H							
I							
J							
K							

FOREIGN PATENT DOCUMENTS							
*	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUB-CLASS	PERTINENT SHTS. PP. DWG. SPEC.
L							
M							
N							
O							
P							
Q							

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)	
R	S. Wolf, "Silicon Processing For The VLSI Era; vol. 2", Lattice Press, 1990, pp. 201-205
S	
T	
U	

EXAMINER <i>Thomas B. Bledau</i>	DATE 8/31/95
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* A copy of this reference is not being furnished with this office action.
(See Manual of Patent Examining Procedure, section 707.05 (a).)